

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Joel S. Emer et al.

Confirmation No.:

Application No.:

Examiner:

Filing Date:

Group Art Unit:

Title: METHOD AND APPARATUS TO QUIESCE A PORTION OF A SIMULTANEOUS  
MULTITHREADED CENTRAL PROCESSOR UNIT

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or  
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97(c) together with either a:  
☐ Statement under 37 CFR 1.97(e), or  
☐ a \$180.00 fee under 37 CFR 1.17(p), or  
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:  
☐ Statement under 37 CFR 1.97(e)(1) or (2), and  
☐ a \$180.00 fee set forth in 37 CFR 1.17(p).  
(Filed after final action, a notice of allowance, on or before payment of the issue fee)

Please charge to Deposit Account **08-2025** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **08-2025** pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Statement together with any required copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56. Copies of references entered in prior applications, U.S. Application No's. 09/411,194 and 10/293,975, are not being provided.

☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individuals(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

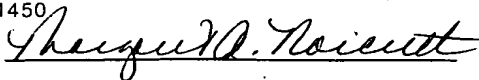
It is requested that the information disclosed herein be made of record in this application.

"Express Mail" label no. **EV 215730695 US**

Date of Deposit **10/07/03**

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

By

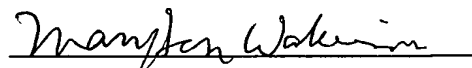


Typed Name: **Margaret A. Norcutt**

Respectfully submitted,

Hamilton, Brook, Smith & Reynolds, P.C.

By



**Mary L u Wakimura**

Attorney/Agent for Applicant(s)

Reg. No. **31,804**

Date: **10-7-03**

<b>FORM PTO-1449</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (Use several sheets if necessary)	ATTY. DOCKET NO. <b>200308288-3</b>	APPLICATION NO.	CONFIRMATION NO.
	APPLICANT <b>Joel S. Emer et al.</b>		
	FILING DATE	GROUP	

### REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A	5,790,851	08/04/98	Frank et al.	
	1B	5,524,247	06/04/96	Mizuno	
	1C	5,546,593	08/13/96	Kimura et al.	
	1D	5,961,584	10/05/99	Wolf	
	1E	5,937,187	08/10/99	Kosche et al.	
	1F	5,872,963	02/16/99	Bitar et al.	
	1G	6,256,659	07/03/01	McLain, Jr. et al.	
	1H	4,642,756	02/10/87	Sherrod	
	1I	4,710,883	12/02/87	Wilson et al.	
	1J	6,128,640	10/03/00	Kleinman	
	1K				

### FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L	EP0511748 A2	04/92	IBM Corporation		
	1M					
	1N					
	1O					
	1P					

### OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	1Q	Tullsen, D. M., et al., "Exploiting Choice: Instruction Fetch and Issue on an Implementable Simultaneous Multithreading Processor," Paper Presented at the Proceedings of the 23rd Annual International Symposium on Computer Architecture, Philadelphia, PA, (May 1996).
	1R	Lo, J.L., et al., "Converting Thread-Level Parallelism to Instruction-level Parallelism via Simultaneous Multithreading," Version of this Paper to appear in ACM Transactions on Computer Systems, pp. 1-25 (August 1997).
	1S	Tullsen, D.M., et al., "Supporting Fine-Grained Synchronization on a Simultaneous Multithreading Processor," UCSD CSE Technical Report (CS98-587) and UW CSE Technical Report (UW-CSE-98-06-02) (June 1998).

EXAMINER

DATE CONSIDERED

FORM PTO-1449

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(Use several sheets if necessary)

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	2A				
	2B				
	2C				
	2D				
	2E				
	2F				
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	2I				
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	2L					
	2M					
	2N					
	2O					
	2P					

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

2Q	Tullsen, D. M. et al., "Simultaneous Multithreading: Maximizing On-Chip Parallelism," Paper Presented at the Proceedings of the 22nd Annual International Symposium on Computer Architecture, Santa Margherita Ligure, Italy (June, 1995).
2R	Eggers, S. J. et al., "Simultaneous Multithreading: A Platform for Next-Generation Processors," IEEE Micro, pps. 12-19, (September/October 1997).
2S	

EXAMINER

DATE CONSIDERED